

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 08-101802
(43)Date of publication of application : 16.04.1996

(51)Int.Cl. G06F 13/14

(21)Application number : 06-235794 (71)Applicant : TOSHIBA CORP
TOSHIBA COMPUT ENG CORP
(22)Date of filing : 30.09.1994 (72)Inventor : NAKAMURA NOBUTAKA
SENUMA KOICHI

(54) COMPUTER SYSTEM

(57)Abstract:

PURPOSE: To easily set or change a memory address map.

CONSTITUTION: The memory address space of a CPU 11 is divided into plural memory address areas and attribute information showing the kinds of memory devices which should be arranged in the memory address areas is set in an attribute register file 201 by the areas. When the CPU 11 executes a bus cycle for memory access, the attribute information in the memory address area corresponding to the value of the memory address from the CPU 11 is read out of the attribute register file 201 and the kind of a memory device to be accessed is determined with the attribute information. Therefore, the memory address map can easily be defined only with the contents of the attribute information of the attribute register file 201.

